

<b>Form PTO-1449</b> (Rev. 2-32)		<b>U.S. Department of Commerce</b> <b>Patent &amp; Trademark Office</b>		<b>Atty. Docket No.</b>  Q65151	<b>Serial No.:</b>  Confirmation No.: Not Assigned
<b>INFORMATION DISCLOSURE STATEMENT</b> (Use several sheets if necessary)				Applicant: Jan Louis Josephina SERVAES, et al.	
				<b>Filing Date:</b> July 23, 2001	<b>Group:</b> Not Assigned

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U.S. PATENT DOCUMENTS							
Examiner Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)	

FOREIGN PATENT DOCUMENTS							
	Document	Date	Country	Class	Sub-class	Translation Yes/No	
WFB WFB	EP 0 901 221 A1	03/10/99	European			Yes	
	WO 99/39433	08/05/99	World Office PCT			Yes	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
WFB	B. Schweber, "Analog Front Ends Bridge the XDSL-to-Real-World Chasm", EDN ELECTRICAL DESIGN NEWS, Massachusetts, US, Vol. 44, No. 7, 1 April 1999, pp. 48, 50, 52, 54, 56, 58, 60, 62, 64, XP000899059
	E. Nash, "Line-Driver Design for Broadband Communications Applications", ELECTRONIC DESIGN, us, Cleveland, Ohio, Vol. 45, No. 27, 1 December 1997, pp. 81-82, 84, 86, 88, XP000755760
	M. Steffes et al, "Optimizing Performance in an XDSL Line Driver", ELECTRONIC DESIGN, Cleveland, Ohio, US, Vol. 47, No. 8, 19 April 1999, pp. 44, 46, 48, 52, 54, 56, 58 XP000893613
V	E. Nash, "Saving Dynamic Power in HDSL Line Driving Applications, ELECTRONIC DESIGN, US Cleveland Ohio, Vol., 45, No. 12, June 9, 1997, page 88, 90, 92, XP000731500

<b>EXAMINER:</b> <i>[Signature]</i>	<b>DATE CONSIDERED:</b> 8/16/04
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